

Electronic Acknowledgement Receipt

EFS ID:	1523823
Application Number:	10814968
International Application Number:	
Confirmation Number:	8696
Title of Invention:	Integrated circuit having a memory cell array capable of simultaneously performing a data read operation and a data write operation
First Named Inventor/Applicant Name:	Kyo-Min Sohn
Customer Number:	22150
Filer:	Frank Chau/Richard D. Ratchford, Jr.
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Attorney Docket Number:	8021-225 (SS-18860-US)
Receipt Date:	16-FEB-2007
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Amendment - After Non-Final Rejection	8021-225Amend2-16-07.pdf	619250	no	10

Warnings:

Information:		
<table border="1"><tr><td>Total Files Size (in bytes):</td><td>619250</td></tr></table>	Total Files Size (in bytes):	619250
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